



PATENT  
Serial No. 10/765,923  
Docket No. 29287/141

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Hiroyuki MIZUNO, *et al.*  
SERIAL NO. : 10/765,923  
FILED : January 29, 2004  
FOR : SEMICONDUCTOR INTEGRATED CIRCUIT  
GROUP ART UNIT : 2816  
EXAMINER : Jeffery Shawn ZWEIZIG

COMMISSIONER FOR PATENTS  
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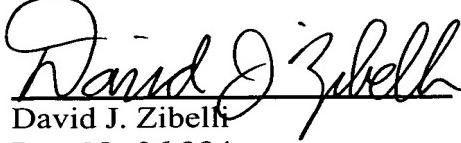
**REQUEST FOR APPROVAL OF DRAWING CORRECTIONS**

SIR:

In response to the Office Action mailed August 12, 2004, enclosed herewith is one (1) sheet of corrected drawing containing Fig. 2, for the above-identified patent application. Approval is requested.

The Commissioner is hereby authorized to charge any fees and credit any overpayments associated with this filing to Deposit Account No. 11-0600.

Respectfully submitted,  
KENYON & KENYON

  
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Dated: January 12, 2005

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